Si3471CDV vs. Si3471DV

Description: **P-Channel, 12-V (D-S) MOSFET**
Package: **TSOP-6**
Pin Out: **Identical**

Part Number Replacements: Si3471CDV-T1-E3 replaces Si3471DV-T1-E3
Si3471CDV-T1-E3 replaces Si3471DV-T1

ABSOLUTE MAXIMUM RATINGS T_A = 25 °C, unless otherwise noted					
PARAMETER		**SYMBOL**	**Si3471CDV**	**Si3471DV**	**UNIT**
Drain-Source Voltage		V_{DS}	- 12	- 12	V
Gate-Source Voltage		V_{GS}	± 8	± 8	
Continuous Drain Current	T_A = 25 °C	I_D	- 7.4	- 6.8	A
	T_A = 70 °C		- 5.9	- 4.9[a]	
Pulsed Drain Current		I_{DM}	- 20	- 20	
Continuous Source Current (MOSFET Diode Conduction)		I_S	- 1.67	- 1.7	
Power Dissipation	T_A = 25 °C	P_D	2.0	2.0	W
	T_A = 70 °C		1.3	1.0[a]	
Operating Junction and Storage Temperature Range		T_J and T_{stg}	- 55 to 150	- 55 to 150	°C
Maximum Junction-to-Ambient		R_{thJA}	62.5	62.5	°C/W

SPECIFICATIONS T_J = 25 °C, unless otherwise noted									
PARAMETER		**SYMBOL**	**Si3471CDV**			**Si3471DV**			**UNIT**
			MIN.	**TYP.**	**MAX.**	**MIN.**	**TYP.**	**MAX.**	
Static									
Gate-Threshold Voltage		$V_{GS(th)}$	- 0.4		- 1.0	- 0.40		- 1	V
Gate-Body Leakage		I_{GSS}			± 100			± 100	nA
Zero Gate Voltage Drain Current		I_{DSS}			- 1			- 1	µA
On-State Drain Current	V_{GS} = - 4.5 V	$I_{D(on)}$	- 20			- 20			A
Drain-Source On-Resistance	V_{GS} = - 4.5 V	$R_{DS(on)}$		0.021	0.026		0.025	0.031	Ω
	V_{GS} = - 2.5 V			0.027	0.036		0.032	0.050	
	V_{GS} = - 1.8 V			0.035	0.048		0.041	0.053	
Forward Transconductance		g_{fs}		26			20		S
Diode Forward Voltage		V_{SD}		- 0.8	- 1.2		- 0.7	- 1.2	V
Dynamic									
Total Gate Charge		Q_g		20	30		18	33	nC
Gate-Source Charge		Q_{gs}		2.7			2.3		
Gate-Drain Charge		Q_{gd}		5.4			4.6		
Gate Resistance		R_g		4.5			NS		Ω

Note

NS denotes parameter not specified in original datasheet
a. T_A = 85 °C

Specification comparisons are supplied as a courtesy to compare two devices and do not constitute a commercial product datasheet or any guarantee of identical performance. Designers should refer to the appropriate datasheets of the same number for guaranteed specification limits.